

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(s): DEPIANO et al.

SERIAL NO.: 10/798,142 ART UNIT:

FILING DATE: March 11, 2004 EXAMINER:

TITLE: OPTICAL WAVELENGTH CONTROL SYSTEM

ATTORNEY

DOCKET NO.: 871-011711-US(PAR)/36030289

Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT (37 C.F.R. §1.97(b)(3))

Sir:

This information disclosure statement is being filed before the mailing of a first Office Action on the merits.

The following information is being disclosed to the Patent and Trademark Office as information that may be material to the examination of the above-identified patent application. Applicants' Attorney is aware of the following references:

US 5,825,792 US 6,377,592

US 6,094,446

US 6,384,947

US 5,781,572 WO 01/11739 JP 07095159

"Design and Manufacturability Issues of a Co-packaged DFB/MZ Module" (pp. 197-200)

Copies of these references are enclosed together with a Form PTO-1449.

The filing of this Statement is not to be construed as a representation that a search has been made regarding the claimed invention (37 C.F.R. §1.97(g)) or that no other possible material information exists. In addition, the filing of this Information Disclosure Statement is not to be construed to be an admission that the information cited in the Statement is, or is considered to be, material to patentability (37 C.F.R. §1.97(h)).

The Commissioner is hereby authorized to charge payment for any fees associated with this communication or credit any over payment to Deposit Account No. 16-1350.

Respectfully submitted,

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date indicated below as first class mail in an envelope address to the Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: 4/14/04

Signature: Shannon Watt

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Page No.: 1 of: 1

INTERMATION DISCLOSURE ATRADE CITATION FORM FOR PATENT APPLICATION (FORM PTO-1449)

Docket No.: 871-011711-US (PAR)

Serial No.: 10/798,142

Applicant(s): DEPIANO et al.

	(FORM FIO-14	7 <i>7)</i>			_		
(Substitute)			Filing Date: March 11, 2004		Group:		
			U.S. PATENTS				
Initials	Patent Number	Issue Date	Name		Class	Sub- class	Filing date
	5,825,792	10/20/98	Villeneuve et al.		372	32	
	6,377,592	4/23/02	Auracher et al.		372	9	
	6,094,446	7/25/00	Tei et al.		372	32	
	6,384,947	5/7/02	Ackerman et al.		359	187	
	5,781,572	7/14/98	Tahara et al.		372	34	
_			U.S. PATENT PUBLICATI	ONS			-
Initials	Publication No.	Pub. Date	Name		Class	Sub- class	Filing Date
		FOI	 REIGN PATENT DOCUME	NTS	<u> </u>		
Initials	Document Number	Date	Country		Name Translati Yes/No/		
	WO 01/11739	15.02.01	PCT	CORE	CORETEK, INC. N/A		
	JP 07095159	04.07.95	Japan	Tokyo Electric Power Co. Inc.		No	
· · · · · · · · · · · · · · · · · · ·	OTHE	R DOCUME	NTS (Title, Author, Date, Pa	ges, Etc	., if knov	vn)	
	K. Anderson, "Design and Technology Conf		rability Issues of a Co-packaged DFI	B/MZ Mod	lule", IEEE	Electronic C	omponents
			atent Office, JP 07095159, Tokyo ele	ctric Powe	r Co. Inc.		
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		red whether or	not citation is in conformance with M	1			considered

Include a copy of this citation form with your next correspondence to the Applicant(s).

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